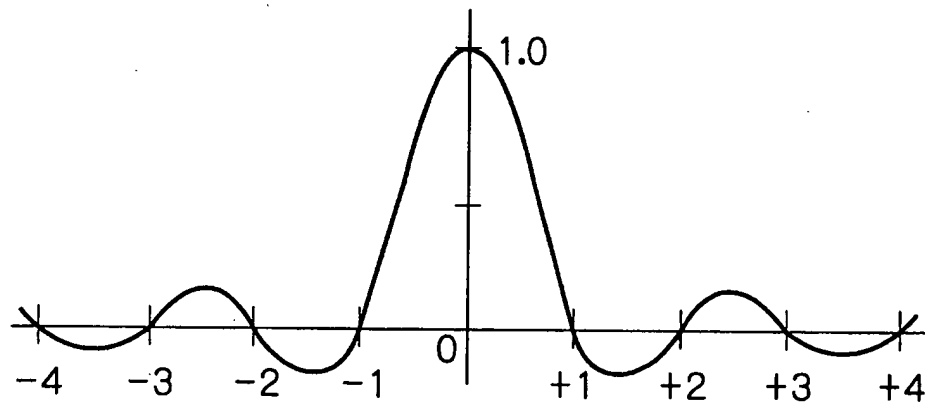
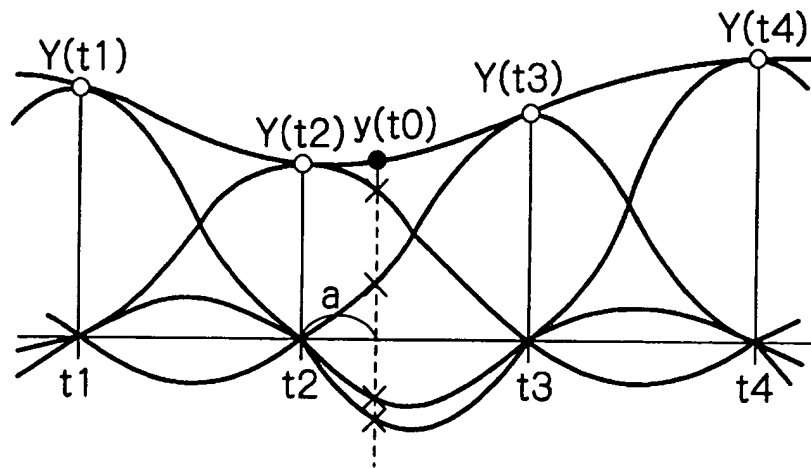


F I G. 1



F I G. 2



F I G. 3

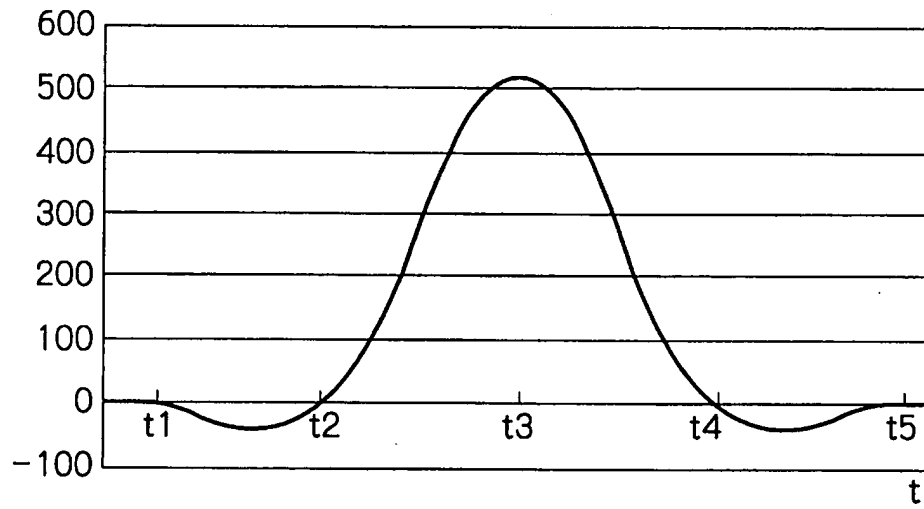
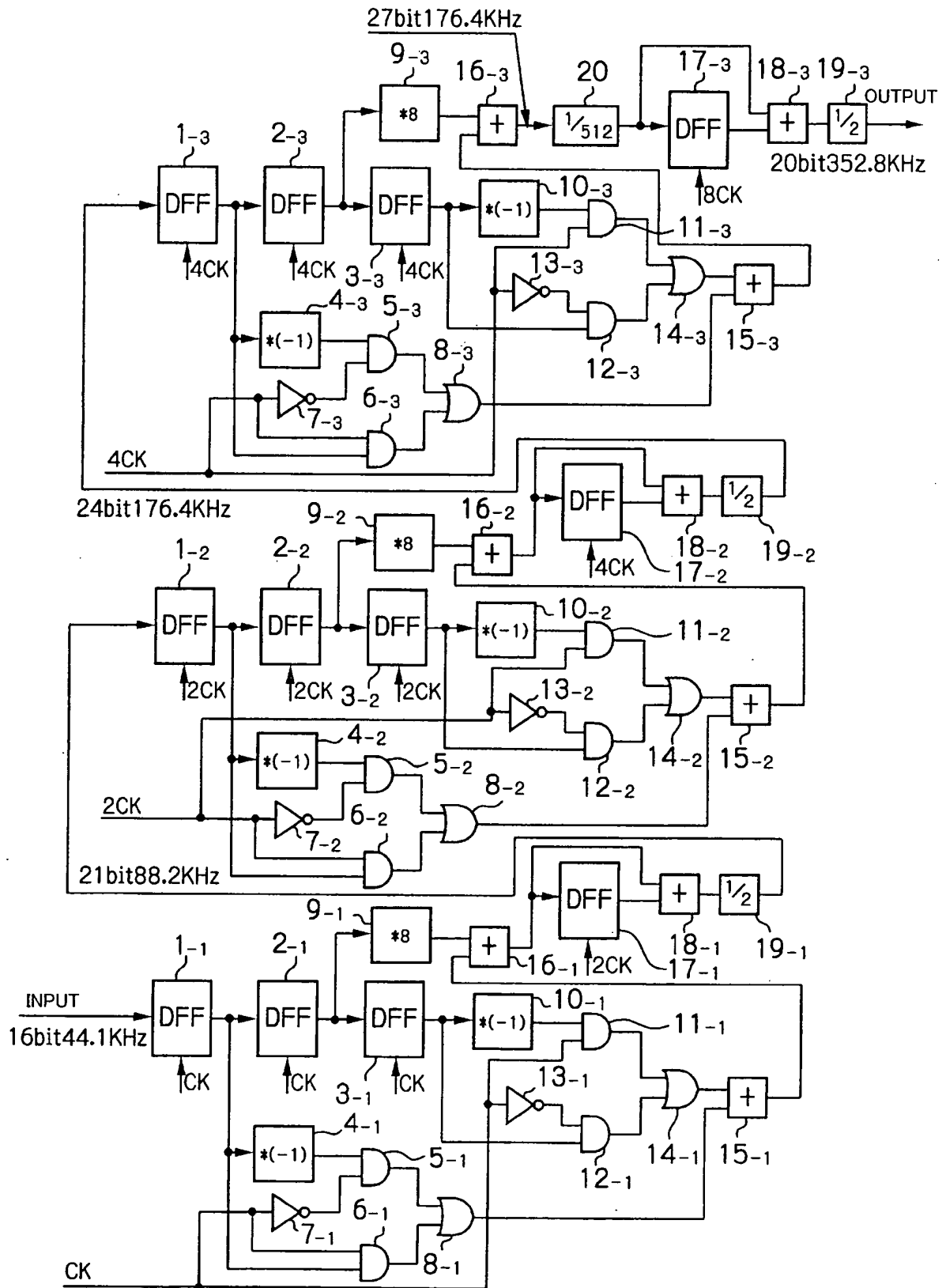


FIG. 4



F I G. 5

	A	B	C	D	E	F	...
	-1						
	1						
a	8	-1					
	8	1					
b	1	8	-1				
	-1	8	1				
c		1	8	-1			
		-1	8	1			
d			1	8	-1		
			-1	8	1		
e				1	8	-1	
				-1	8	1	
f					1	8	
					-1	8	
						1	
						-1	
⋮							

F I G. 6

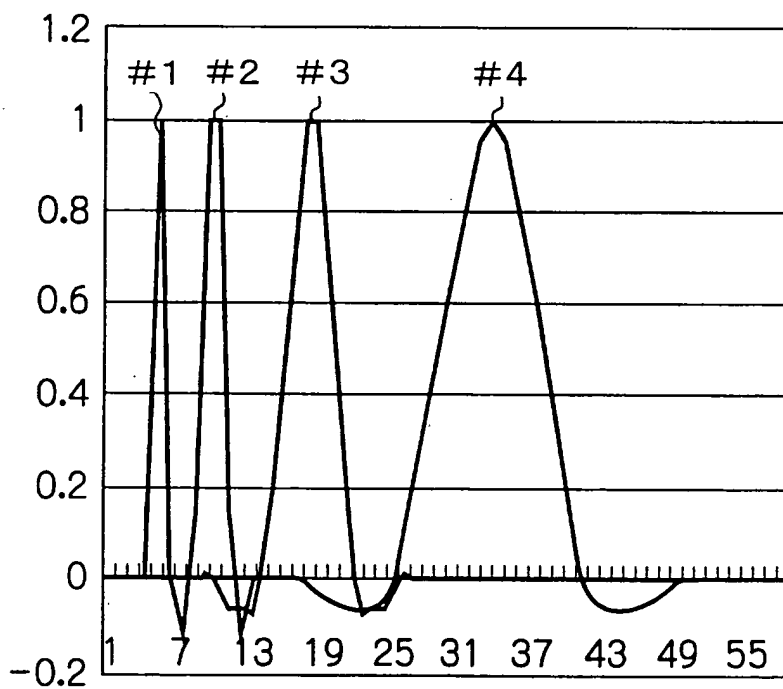


FIG. 7

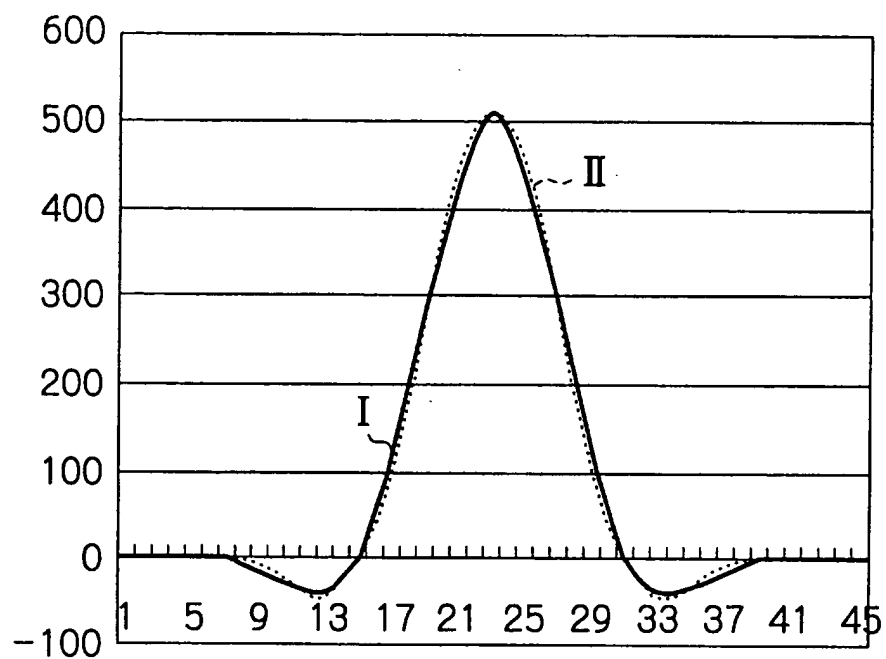
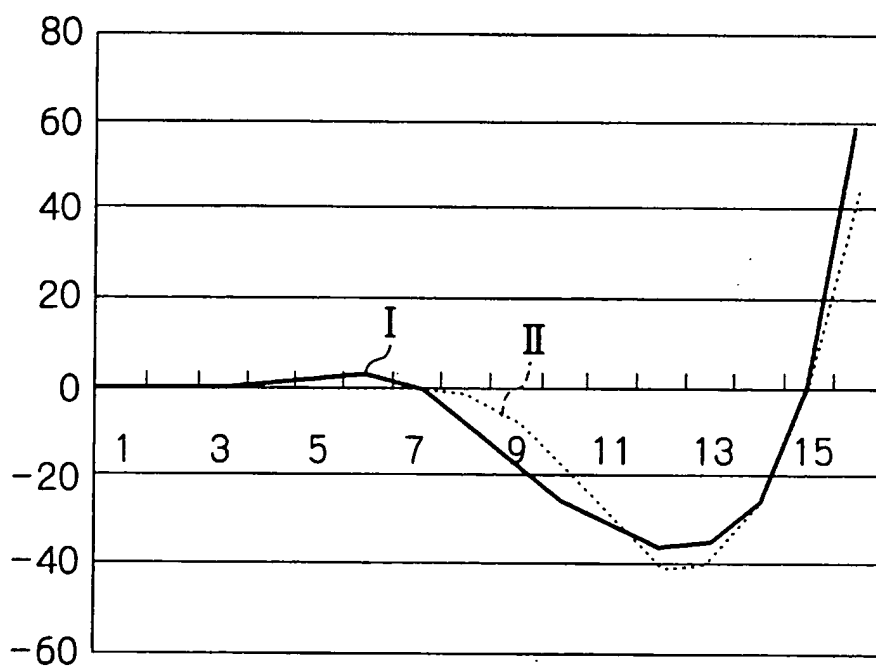
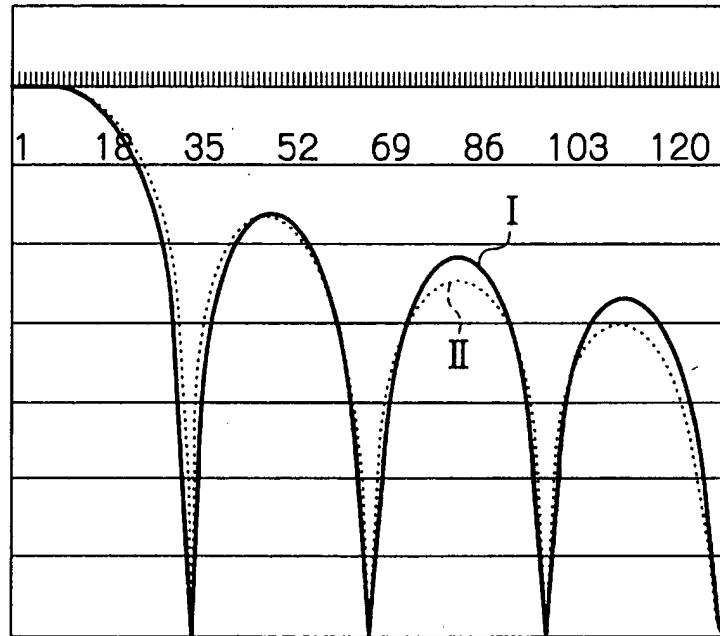


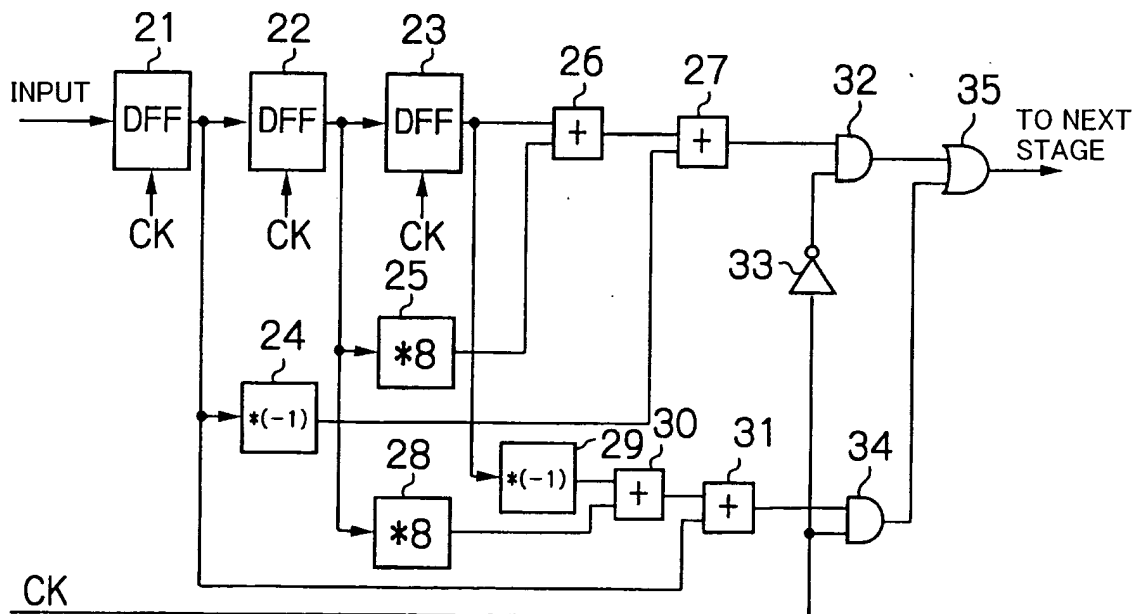
FIG. 8



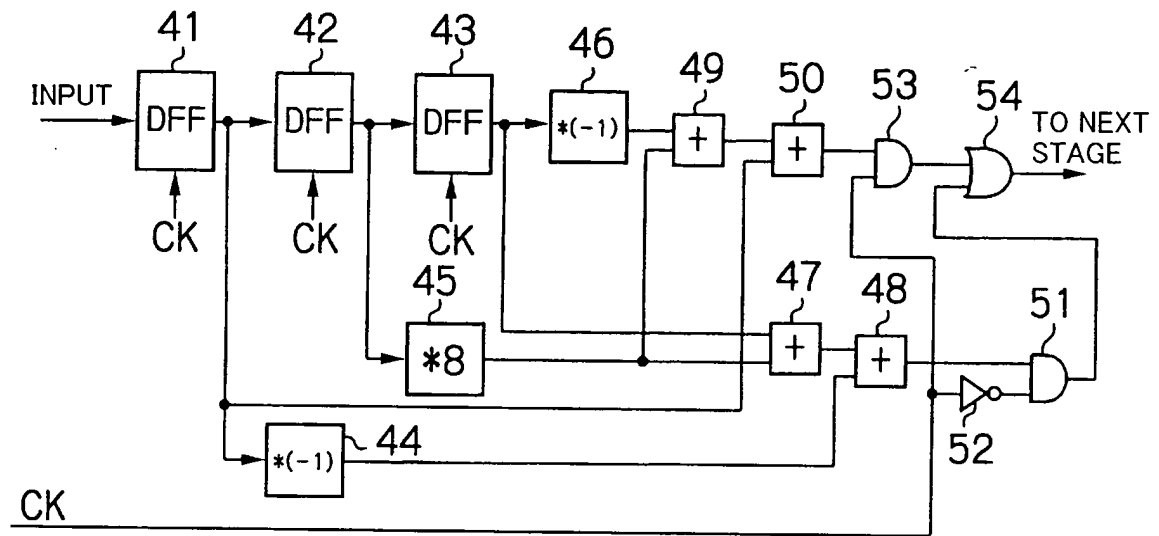
F I G. 9



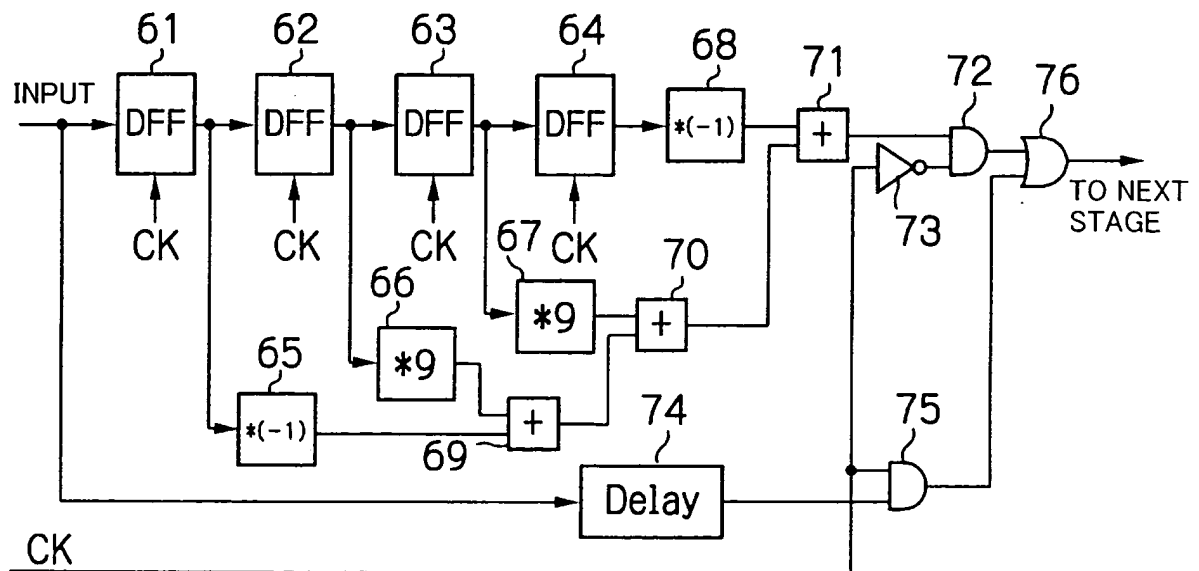
F I G. 10



F I G. 1 1



F I G. 1 2



F I G. 1 3

	A	B	C	D	E	F
a	9	-1					
	-16	0					
b	9	9	-1				
	0	-16	0				
c	1	9	9	-1			
	-1	0	-16	0			
d		-1	9	9	-1		
			0	-16	0		
e			-1	9	9	-1	
				0	-16	0	
f				-1	9	9	
					0	-16	
					-1	9	
						0	
						-1	
⋮							

Figure 1 is a block diagram of a 3-stage pipeline for a 32768-point FFT. The diagram is divided into three horizontal sections: 4CK, 2CK, and CK. Each section contains a series of DFFs (Data Flow Filters) and arithmetic operations (addition, subtraction, multiplication by 9, 25, and -1). The 4CK section has 6 DFFs (81-3 to 85-3) and 6 multipliers (86-3 to 95-3). The 2CK section has 5 DFFs (81-2 to 85-2) and 5 multipliers (86-2 to 95-2). The CK section has 5 DFFs (81-1 to 85-1) and 5 multipliers (86-1 to 95-1). The final output is a 32768-point FFT result.

F I G. 1 5

